

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

|                                   |   |                 |
|-----------------------------------|---|-----------------|
| In re Application of:             | ) |                 |
|                                   | ) |                 |
| <b>Koichi FUJISAKI</b>            | ) |                 |
|                                   | ) | Group Art Unit: |
| Application No.: Not Yet Assigned | ) |                 |
|                                   | ) | Examiner:       |
| Filed: <b>May 20, 2004</b>        | ) |                 |
|                                   | ) |                 |
| For: <b>DATA PROCESSING</b>       | ) |                 |
| <b>APPARATUS AND LOGICAL</b>      | ) |                 |
| <b>OPERATION APPARATUS</b>        | ) |                 |

**Commissioner for Patents**  
**P.O. Box 1450**  
**Alexandria, VA 22313-1450**

Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicant brings to the Examiner's attention the documents listed on attached Form PTO-1449. Copies of the listed documents are attached. Applicant respectfully request that the Examiner consider the documents listed on attached Form PTO-1449 and indicate that they were considered by making an appropriate notation on this form.

This Information Disclosure Statement is being filed with the above-referenced application.

The following is listed on the accompanying PTO-1449 and is in a non-English language:

1. Japanese patent application publication no. 2000-066585. The relevance of this document is discussed at pages 6-7 of the specification of the present application. An English language abstract of this document is also enclosed.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and applicant determines that the cited documents do not constitute "prior art" under United States law, applicant reserves the right to present to the Office the relevant facts and law regarding the appropriate status of such documents. Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: May 20, 2004

By: 

Richard V. Burgujian  
Reg. No. 31,744

Enclosures  
RVB/FPD/blc

## INFORMATION DISCLOSURE CITATION

|                  |                 |                 |  |
|------------------|-----------------|-----------------|--|
| Atty. Docket No. | 05225.0263      | Application No. |  |
| Applicant        | Koichi FUJISAKI |                 |  |
| Filing Date      | May 20, 2004    | Group:          |  |

| U.S. PATENT DOCUMENTS |  |                 |            |      |       |           |                            |
|-----------------------|--|-----------------|------------|------|-------|-----------|----------------------------|
| Examiner Initial*     |  | Document Number | Issue Date | Name | Class | Sub Class | Filing Date If Appropriate |
|                       |  |                 |            |      |       |           |                            |

| FOREIGN PATENT DOCUMENTS |  |                 |                  |         |       |           |                       |
|--------------------------|--|-----------------|------------------|---------|-------|-----------|-----------------------|
|                          |  | Document Number | Publication Date | Country | Class | Sub Class | Translation Yes or No |
|                          |  | 2000-066585     | March 3, 2000    | Japan   |       |           | Abstract              |
|                          |  |                 |                  |         |       |           |                       |

| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) |  |
|--|--|
|  | Paul KOCHER et al.; "Differential Power Analysis", Advances in Cryptology of CRYPTO '99, Springer-Verlag Lecture notes in Computer Science Vol. 1666, Pages 388-398 (1999) |
|  | Tomohiro YONEDA; "Abstracted Instruction Cache of TITAC2 - As A Benchmark For Timed Asynchronous Circuit Verification", TR99-0002, (June 1999).                            |
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|---|---|
| Examiner  | Date Considered   |
| *Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. |   |
| Form PTO 1449   | Patent and Trademark Office - U.S. Department of Commerce |